



- 2015 CONTROLA Day four
- Lab. For Navigation, Control, and Applications
- Yang-Yu-Young

# Ubuntu 기반 ARM 개발환경 구축 & Controla-9DOF 회로도 설계

# Contents

- I. What is Ubuntu?
- II. Build a development environment

What is Ubuntu?

하나. 무료이다.

둘. 공개운영체제이다.

셋. 프로그래밍 용으로는 적합하다.

넷. 다중작업이 가능하다.

다섯. 업데이트가 빠르다.



Build a development  
environment

- Tool Chain 설치
- 보드 Firmware 다운
- ST-link 유틸리티 설치
- Sample code 다운
- Compile
- 보드에 다운로드

## Tool Chain 설치

### **Tool Chain**이란?

Embedded system에서 자주 사용되는 말이며, Target 시스템의 Software 개발을 진행하기 위해 필요한 Host system의 Cross Compile 환경이다.

Toolchain은 컴파일러만을 의미하지 않는다. Source code를 Compile하고 Build실행 파일을 생성하는데 필요한 각종 Utility 및 Library의 모음이라고 생각하면 되고, 기본적으로는 Assembler, Linker, C Compiler, C library 등으로 구성되어 있다.

## Tool Chain 설치

### **Cross** 란?

현재 우리가 사용하고 있는 PC의 CPU와 Atmega, Mango에서 사용하는 CPU는 다르다. 이와 같이 개발하는 곳의 환경과 실제 개발된 코드가 동작될 환경이 다를 경우 Cross라고 부른다.

## Tool Chain 설치

```
yyy@controla:~$ sudo add-apt-repository ppa:terry.guo/gcc-arm-embedded  
yyy@controla:~$ sudo apt-get update  
yyy@controla:~$ sudo apt-get install gcc-arm-none-eabi  
yyy@controla:~$ arm-none-eabi-gcc --version
```

## 보드 Firmware 패키지 및 예제 다운로드

<http://www.st.com/web/en/catalog/tools/PF257904> 에서 보드 Firmware 다운

Sample & Buy <span style="float: right;">Top</span>			
Part Number	Version	Marketing Status	Download
STSW-STM32068	1.1.0	Active	<a href="#">Download</a>

## 보드 Firmware 패키지 및 예제 다운로드

```
yyy@controla:~$ unzip stsw-stm32068.zip  
yyy@controla:~$ sudo mv STM32F4-Discovery_FW_V1.1.0/ /opt/
```

예제코드 위치

```
yyy@controla:~$ ls /opt/STM32F4-Discovery_FW_V1.1.0/Project/Peripheral_Examples/
```

## ST-link 유틸리티 설치

```
yyy@controla:~$ git clone https://github.com/texane/stlink.git
yyy@controla:~$ cd stlink
yyy@controla:~/stlink$ sudo apt-get install autoconf
yyy@controla:~/stlink$ ./autogen.sh
yyy@controla:~/stlink$ ./configure
yyy@controla:~/stlink$ make
yyy@controla:~/stlink$ sudo make install
```

## Sample code 다운

```
yyy@controla:~/stlink$ git clone https://github.com/Malkavian/tuts.git
yyy@controla:~/stlink$ cd tutorials/stm/blink
yyy@controla:~/stlink/tutorials/stm/blink$ ls -l
```

합계 56

```
-rwxrwxr-x 1 webnautes webnautes 3816 1월 11 17:37 Makefile
-rw-rw-r-- 1 webnautes webnautes 7700 1월 11 17:37 Readme.md
-rw-rw-r-- 1 webnautes webnautes 5490 1월 11 17:37 main.c
-rwxrwxr-x 1 webnautes webnautes 4823 1월 11 17:37 stm32_flash.ld
-rwxrwxr-x 1 webnautes webnautes 3870 1월 11 17:37 stm32f4xx_conf.h
-rwxrwxr-x 1 webnautes webnautes 21726 1월 11 17:37 system_stm32f4xx.c
```

## Sample code 다운

Makefile 에서 보드 Firmware 복사해둔 위치로 수정

```
# This is the directory containing the firmware package,  
# the unzipped folder downloaded from here:  
# http://www.st.com/web/en/catalog/tools/PF257904  
STM_DIR=/opt/STM32F4-Discovery_FW_V1.1.0
```

Makefile 에서 Toolchain설정 부분 수정

```
# The tool we use  
CC      = arm-none-eabi-gcc  
OBJCOPY = arm-none-eabi-objcopy  
GDB     = arm-none-eabi-gdb
```

# Compile

```
yyy@controla:~/stlink/tuts/stm/blinkyy$ make
```

```
arm-none-eabi-gcc -I/opt/STM32F4-Discovery_FW_V1.1.0/Utilities/STM32F4-Discovery -I/opt/STM32F4-Discovery_FW_V1.1.0/Libraries/CMSIS/Include -I/opt/STM32F4-Discovery_FW_V1.1.0/Libraries/CMSIS/ST/STM32F4xx/Include -I/opt/STM32F4-Discovery_FW_V1.1.0/Libraries/STM32F4xx_StdPeriph_Driver/inc -I-DUSE_STDPERIPH_DRIVER -ggdb -O0 -Wall -Wextra -Warray-bounds -mlittle-endian -mthumb -mcpu=cortex-m4 -mthumb-interwork -mfloat-abi=hard -mfpu=fpv4-sp-d16 -Tstm32_flash.ld main.c system_stm32f4xx.c /opt/STM32F4-Discovery_FW_V1.1.0/Libraries/STM32F4xx_StdPeriph_Driver/src/stm32f4xx_rcc.c /opt/STM32F4-Discovery_FW_V1.1.0/Libraries/STM32F4xx_StdPeriph_Driver/src/stm32f4xx_gpio.c /opt/STM32F4-Discovery_FW_V1.1.0/Libraries/CMSIS/ST/STM32F4xx/Source/Templates/TrueSTUDIO/startup_stm32f4xx.s -o blinky.elf
```

```
arm-none-eabi-objcopy -O ihex blinky.elf blinky.hex  
arm-none-eabi-objcopy -O binary blinky.elf blinky.bin
```

## 보드에 다운로드

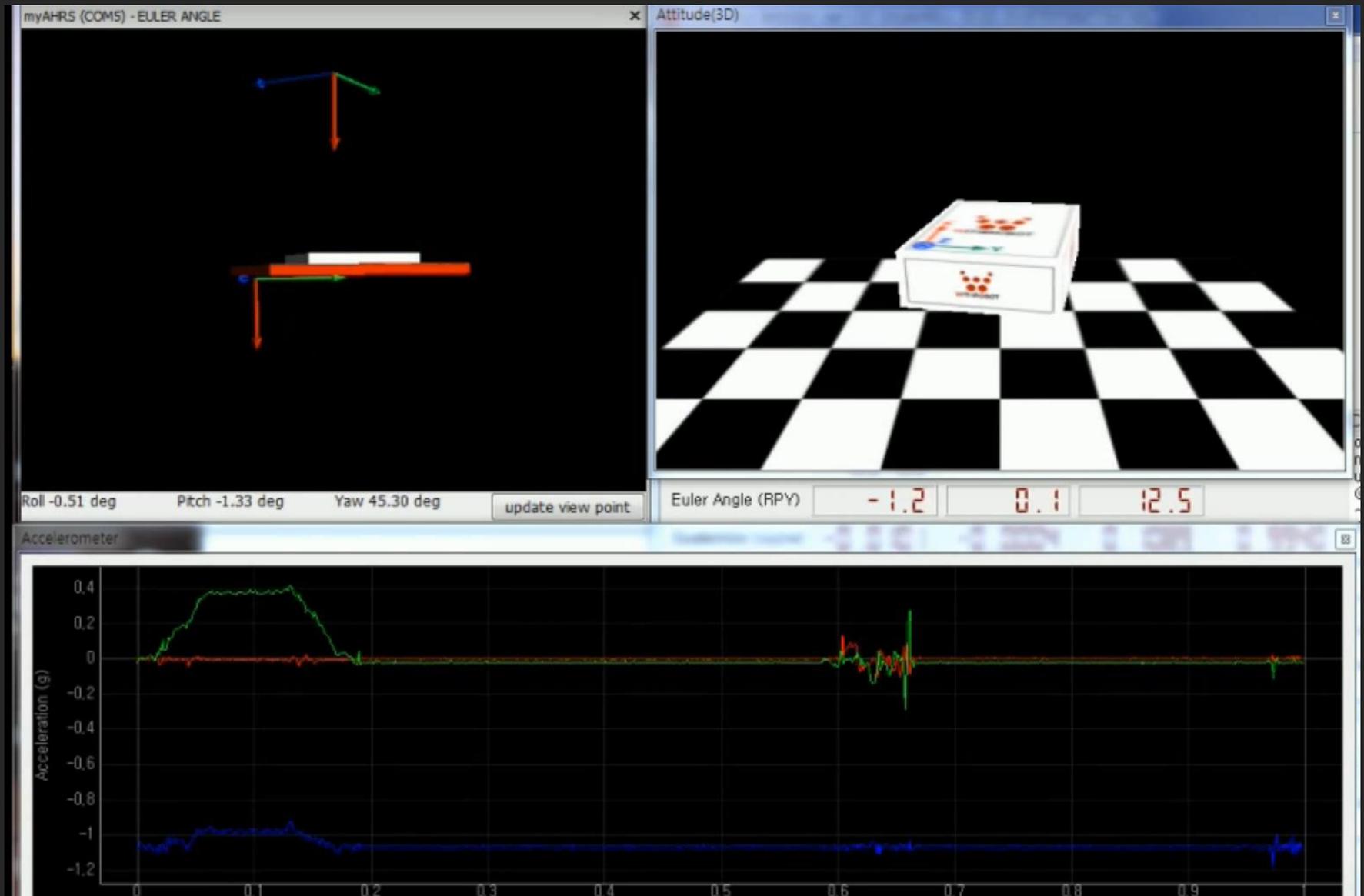
```
yyy@controla:~/stlink/tuts/stm/blinkysudo make flash
```

# Contents

- I. Competition
- II. Controla-9DOF  
Schematic Description

Competition

# 2.1 Competition (1/2)





VPE DISABLED

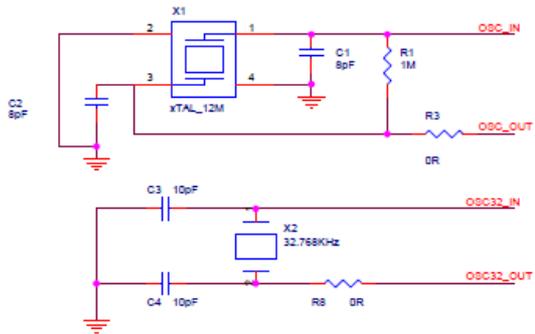


VPE ENABLED

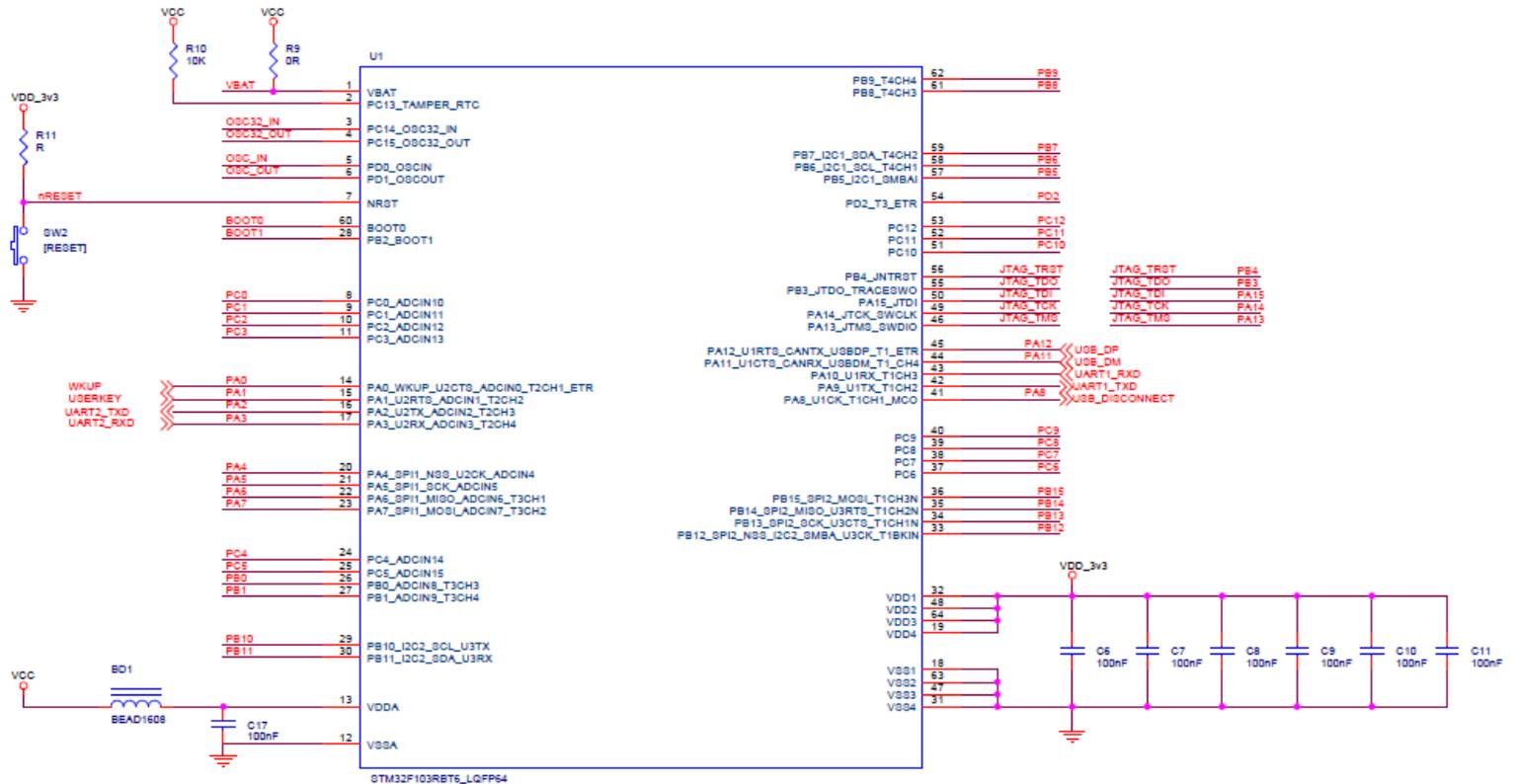
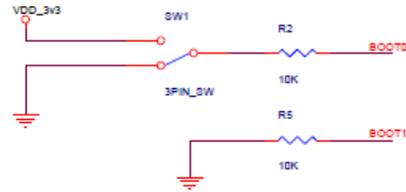
# Controla-9DOF Schematic Description

# 2.2 | Controla-9DOF Schematic Description (1/3)

## Clocks

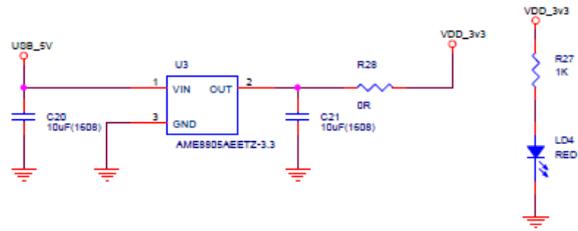


## Boot Mode Selection

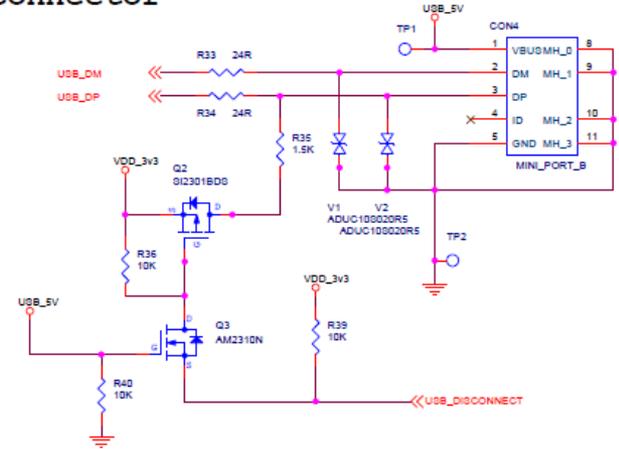


## 2.2 | Controla-9DOF Schematic Description (2/3)

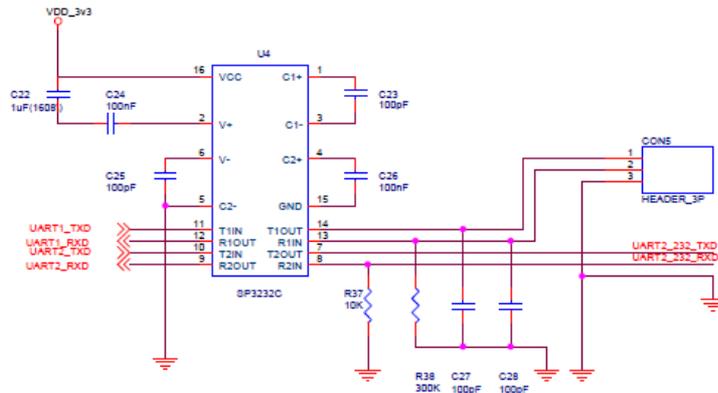
### Main Power



### USE Connector

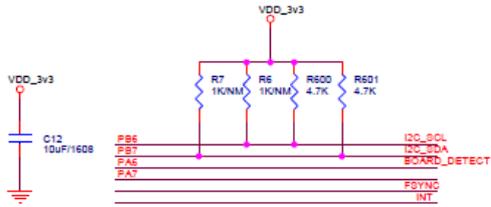


### RS-232C

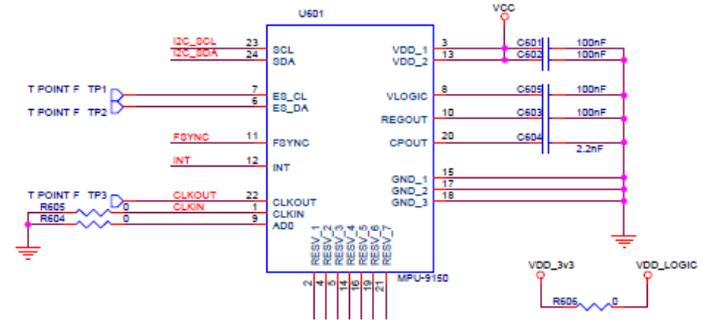


# 2.2 | Controla-9DOF Schematic Description (3/3)

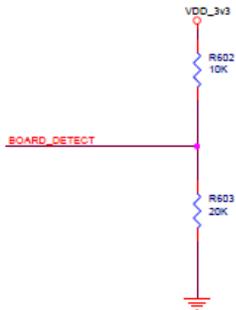
## Connecter



## Sensor



## Board Detect



THANKS.

---